



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re patent application of

Docket No.: FIS920030184US1

Dureseti Chidambarao, et al.

Serial No.: 10/605,135

Group Art Unit: 2811

Filed: September 10, 2003

Examiner: Unassigned

For: **METHOD AND STRUCTURE FOR IMPROVED MOSFETs USING  
POLY/SILICIDE GATE HEIGHT CONTROL**

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

**SUBMISSION OF FORMAL DRAWINGS**

Sir:

Submitted herewith, are six (6) sheets of formal drawings comprising figures 1-4 for the above-identified patent application. Please substitute the formal drawings for the drawings which were filed with the application.

Respectfully submitted,

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